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**REMARKS**

Claims 2-3, 7-11, 15, 17-19, 21, and 25-50 are pending in the present application after this amendment cancels claim 4. Claims 2, 7, 8, 15, 17, 21, 25, 46, and 49 have been amended to correct typographic errors, to change claim dependencies, and/or to clarify the subject matter recited therein. No new matter is added by the amendments and new claims, which find support throughout the specification and figures. In view of the amendments and the following remarks, favorable reconsideration of this case is respectfully requested.

Applicants note with appreciation that the Examiner has allowed claims 9 and 10 and determined that claims 7, 8, 15, 21, 25-44, and 48-50 are directed to allowable subject matter. Applicants hereby acknowledge the Examiner's reasons for indicating allowable subject matter. Applicants respectfully note that there may be additional reasons for allowing these claims that have not been specifically cited in addition to or instead of the cited reasons. Additionally, claims 7, 8, 15, and 21 have been amended into independent form, and therefore these claims are now in condition for allowance.

The Examiner objects to claims 25, 46, and 49 as lacking antecedent basis for some terms. These claims have been amended to resolve the antecedent basis issue, and therefore it is respectfully submitted that these claims, as well as claims 26-44, 48, and 50 which depend therefrom, are now in condition for allowance.

Claims 2, 3, 11, 17-19, and 45-47 (claim 4 having been canceled) are rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,172,895 to Brown et al. (hereinafter Brown). Applicants respectfully traverse.

Claim 2 relates to a memory module comprising a plurality of memory devices, which share a bus line, on a board. In claim 2, the bus line connects terminals of the plurality of memory

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devices in a stubless configuration and an end of the bus line is terminated, and at least a part of the bus line is a strip line. In amended claim 2, *at least one memory device includes a termination circuit, and the memory device in which the termination circuit is included terminates the bus line.*

Applicants respectfully submit that Brown does not disclose, or even suggest, the feature of a memory device including a termination circuit and terminating the bus line. The Examiner asserts that Brown discloses this feature of canceled claim 4 at column 3, lines 1-9 (Office Action; page 3, lines 1-13). However, this section of Brown states only that:

The present invention also allows integration of all of the memory chips that a channel can have onto a single, *terminated module*, which leads to better system integrity and lower cost. The inventive, *self-terminated module* needs only half of the I/O connections of a conventional module of the prior art. Using a conventional prior art connector on a module, two channels of memory can be integrated onto one module, which yields increased bandwidth and double the memory capacity.

(Brown; col. 3, lines 1-9). This section of Brown does not disclose or suggest a memory device having a termination circuit, but rather discusses *a module, that includes all of the memory chips, being terminated*. The disclosure in Brown does not anticipate a memory device including a termination circuit. There is no mention in Brown of *a memory chip or other memory device including a termination circuit*. The Examiner asserts that elements 28 of Brown disclose memory devices (Office Action; page 3, line 2). Since Brown does not disclose or suggest memory devices 28 including a termination circuit and terminating the bus line, Brown does not anticipate amended claim 2. In fact, Brown apparently discloses a resistor terminating the bus line in figure 3, and resistor 52 is distinct from elements 28 as shown in figure 3. Therefore Brown

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does not disclose a memory device including a termination circuit and terminating the bus line.  
Therefore claim 2 is allowable at least for this reason.

Claims 3, 11, 17-19, and 45-47 depend from claim 2 and therefore these claims are allowable for at least the same reasons as claim 2 is allowable.

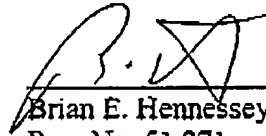
### CONCLUSION

An earnest effort has been made to be fully responsive to the Examiner's objections. In view of the above amendments and remarks, it is believed that the claims are in condition for allowance. Passage of this case to allowance is earnestly solicited.

However, if for any reason the Examiner should consider this application not to be in condition for allowance, he is respectfully requested to telephone the undersigned attorney at the number listed below prior to issuing a further Action.

Any fee due with this paper may be charged on Deposit Account 50-1290.

Respectfully submitted,

  
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Brian E. Hennessey  
Reg. No. 51,271

CUSTOMER NO.: 026304

Phone No.: (212) 940-6311

Fax No.: (212) 940-8986/7

ATTORNEY DOCKET NO.: NEKO 20.511

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